

D \ H	1	2	3	4	5
I	III D				III B
II		III B		III D	
III	III B				
IV				III D	
V	III D			III B	
VI			III B		III D

III C.S 'B' & 'D' - Computer Organization
 and Architecture
 UBRICSMBE2.

- 10 hours.

III C.S 'B' - 49 students (Boys)
 III C.S 'D' - 54 students (Boys)

Major Based Elective Course - III

Computer Organization and Architecture - UBRICSM BE2

Unit-I: Number Systems

Number systems - decimal number system -
Octal & Hexadecimal nos - 1's & 2's comp -
Binary fixed - point representation -
Arithmetic operation on Binary nos, Overflow
& Underflow.

Unit-II: Representation of codes & logic gates.

Floating pt representation - codes -
ASCII, EBCDIC codes, Gray codes,
Excess-3 & BCD, Error detection & correcting
codes, Logic gates - AND, OR, NOT, NOR
and their truth tables, NAND, NOR & XOR
gates.

Unit-III: Flip-flops

Flip-flops - RS, D, JK & T flip-flops,
Registers, Shift register - Multiplexer,
Demultiplexer - Encoder - Decoder - Counter.

Unit-IV: Boolean Algebra.

Basic operations & Boolean laws -
De-Morgan's theorem, K-Map, SOP & POS.
Combinational & Sequential circuits -
Half adder, full adder. Half subtractor
& full subtractor.

Unit-V: DMA.

13/5/20
Pr B-2hr.

Instruction

- operand types
- Addressing modes
- Instruction types
- Instruction formats.

Number of Addresses

- 3-address m/c
- 2-address m/c
- 1-address m/c
- 0-address m/c

(Signature)

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(Faint, mostly illegible handwritten notes, possibly describing instruction formats or addressing modes.)

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Assistant Professor,
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Sivapuram, Pudukkottai - 622 422.

Database System Concepts

Unit I Introduction

Database System Applications -
Purpose of DB systems - View of Data -
DB Languages - Relational DBs - DB
Design - object based and Semis
Structured DBs - Data Storage and
Querying Transaction Management -
Data Mining and Analysis - DB Architecture
DB Users and Administrators - History
of DB Systems.

Unit II Relational Model

Structure of Relational DBs - Fundamentals
Relational Algebra Operations - Additional
Relational - Algebra Operations - Extended
Relational - Algebra Operations - Null
values - Modifications of the DB.

Unit III SQL

Data Definition - Basic Structure
of SQL Queries - Set operations -
Aggregate Functions - Null values - Nested
Queries - Views

17/5/22

III hr.

The Principal Sources of Optimization

- * Quicksort
- * Causes of Redundancy
- * Semantics - Preserving Transformation
- * Global Common Subexpressions
- * Copy Propagation
- * Dead-code Elimination
- * Induction Variables and Reduction in Strength

Constant Propagation

- Constant Propagation is a forward data-flow problem.

- Data flow values for the constant-propagation framework.

- The next for just constant Propagation

L. Subramanian
Dr. S. SUDHA, M.Sc., M.Phil., Ph.D., SET
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MS. H. Jotli

A.P | CS

JJ(CS(A)

Lesson plan.

Even Semester.

ms

ms

ms

ms

Microprocessor and its Applications

UGRICSCC13

Unit I Introduction to Microprocessors

Evolution of Microprocessors - Single chip
Microcontroller - μ processor applications - Programming
Digital Computers - Memory - Buses - Memory
addressing capacity and CPU - μ computers -
Processor Architecture - Intel 8085 - Instruction
cycle - Timing diagram

Unit II Intel 8085

Instruction set of 8085 - Instruction and
Data formats - Addressing modes - Status flags -
Intel 8085 instructions - Programming of microprocessors
- Assembly language - Assemblers - Stacks & Subroutines
- MACRO - Microprogramming

Unit III Examples of Assembly Language Programs

Assembly Language Programming - Simple examples
Addition and Subtraction of Binary and Decimal
Numbers - Complements - Shift - Masking - Finding
the largest and smallest numbers in an Array -
Arranging a series of numbers - Sum of a series
of numbers - Multiplication - Division - Multibyte
Addition and Subtraction.

Unit IV Peripheral Devices and their Interfacing

Peripheral devices and Interfacing - Address
Decoding and Interfacing

⇒ Program

⇒ Explanation

Microprocessor-based Traffic Control

- Introduction

⇒ How traffic control works

- Simple arrangement and
Port connections for microprocessor
based traffic control.

- Program

- Explanation

G.D.

S. Sudha
13/5/22
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Dr. V. VANI M.Sc. M.Phil. P.Ed. Ph.D.
 Assistant Professor
 Department of Computer Science
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Q/H	1	2	3	4	5
I	II B (vac)	III D	II B	I PU	III D
II		II B (vac)	II B Lab Prac		
III	II B	III D		I PU (vac)	
IV	I PU	III D		II B (vac)	III D
V	II B	I PU		III D Lab Prac	
VI	II B	I PU (vac)			II B (vac)

Theory : 15

Lab : 5

20

Database System Concepts

U4R1CS07

UNIT-I

Introduction

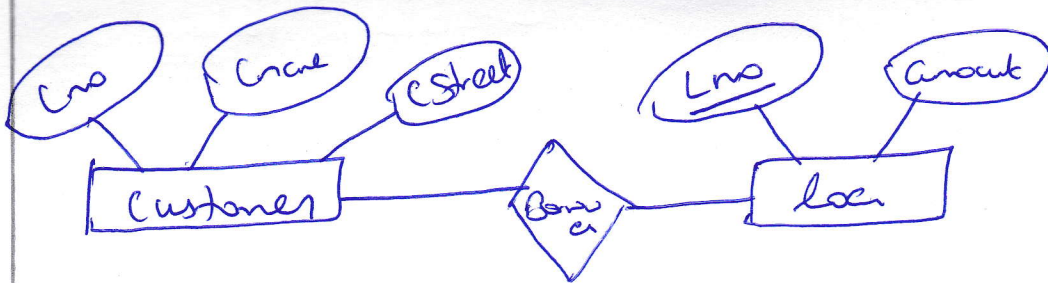
Database System applications - Purpose of Database Systems - View of data - Database Languages - Relational Databases - Database design - Object Based and Semistructured databases - Data Storage and Query Transform Management - Data Mining and analysis - Database Architecture - Database users and Administrators - history of database systems

UNIT-II Relational Model

Structure of Relational databases - Fundamental relational algebra operations - Additional relational algebra operations - Extended relational algebra operations - null values - Modifications of the database.

UNIT-III SQL

Data Definition - Basic Structure of SQL queries



- ↳ Ternary Relationship
- ↳ attributes
- ↳ mapping constraints

III BSc B'1
 sec
 Day 10/11/22
 I/q
 12/05/22

Attributes

- ↳ Basic
- ↳ Types
 - ↳ Simple and Composite
 - ↳ Single and Multivalued
 - ↳ Derived attribute
 - ↳ null attribute

↳ Mapping Constraints

- ↳ one-to-one
- ↳ one-to-many
- ↳ Many-to-one
- ↳ Many-to-many

M. NITHYAKALYANI MEKALA

Notes of Lessons

Feb 2022 to May 2022

Functional Dependency Theory

Closure of Set of functional Dependencies

Logically implied by F

$A \rightarrow B, A \rightarrow C, C \rightarrow H, \text{ and } G \rightarrow I$

If $A \rightarrow H$ means hold relation functional

Dependency follows $A \rightarrow B$, F⁺ means

logically implied by $(A \rightarrow B) \text{ then } t_1[A] = t_2[B]$

Axioms,

Armstrong Axioms

13/5/22
Dhr

Reflexivity rule:

α is set of attributes and $B \subseteq \alpha$ then $\alpha \rightarrow B$ holds

Augmentation rule:

β holds and γ is a set of attributes then $\gamma \alpha \rightarrow \gamma \beta$ holds

Transitivity rule:

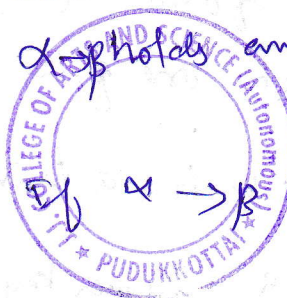
If $\alpha \rightarrow \beta$ holds and $\beta \rightarrow \gamma$ holds then $\alpha \rightarrow \gamma$ holds

Decomposition rule:

If $\alpha \rightarrow \beta$ holds and $\beta \rightarrow \gamma$ holds then $\alpha \rightarrow \gamma$ holds

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13/5/22
Dhr

Name: S. Nithya,
Department: Computer science

Even Semester.

Date :- 13.05.22

Day :- DS

Hour :- I

Topic :- Revision

⊙ Model Exam Question Paper Discussion

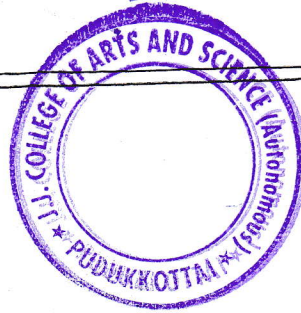
⊙ FAQs Discussion

⊙ All the Units Summarization

⊙ Study Plan Discussion .

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Dr. R. BHUVANESWARI
Dept of Computer Science
JT College of Arts & Science

Feb-22 to May-2022

Unit-I Introduction to Microprocessors

Evaluation of Microprocessors - Single chip Microcomputer Microprocessor Applications - Programming Digital Computers - Memory - Buses - Memory addressing Capacity and CPU - Microcomputers - Processor Architecture - Intel 8085 - Instruction Cycle - Timing diagram.

Unit-II Intel 8085

Instruction set of Intel 8085 - Instruction and Data Formats - Addressing Modes - status flags - Intel 8085 Instructions - Programming of Microprocessors - Assembly language - Assemblers - Stacks and Subroutines - MACRO - Micro Programming.

Unit-III Examples of Assembly Language Programs.

Assembly language Programming - Sample Examples - Addition and Subtraction of Binary and Decimal Numbers - Complements - Shift - Masking - Finding the largest and smallest numbers in an Array - Arranging a series of numbers - Sum of a series of Numbers - Multiplication - Division - Multibyte Addition and Subtraction.

Date: 12/5/22

Class: III B, C

Hrs: 2, 5

TOPIC: Temperature measurement and Control.

Temperature Control is a process in with. change of temperature of a space.

- * To Control temperature.
- * To Minimize manual intervention. using an intelligent process.
- * It Controls the temperature of any industrial plant.

Date 13/5/22

Class: III B, C

Hrs: 1, 5

TOPIC: Microprocessor based Traffic Control.

* A traffic signal typically collected by a controller inside a cabinet mounted on a concrete pad.

* Fixed Time Control.

* Dynamic Control.

* Working Program:

* Source Program.

* Delay Subroutine.

K. RAMESH,

"Notes of lesson."

Note Book.

2021-2022.

Even Semesters.

Computer Organization and

Architecture.

Unit - I: Number Systems.

Number Systems - Decimal Number System, Binary number system, Octal & Hexa-decimal number system, 1's & 2's Complement, Binary Fixed-Point Representation, Arithmetic Operation on Binary numbers, Overflow & Underflow.

Unit - II: Representation of codes and Logic gates

Floating Point Representation, Codes, ASCII, EBCDIC - Codes, Gray Code, Excess-3 & BCD, Error detection & Correction Codes, Logic gates, AND, OR, NOT GATES and their Truth tables, NOR, NAND & XOR gates.

Unit - III: Flip-Flops.

Flip-flops - RS, D, JK & T Flip-flops

13.5.22
05/10

Simple HTML Program ;

Open Notepad to type

EM/:

```
< !DOCTYPE html >
```

```
< html >
```

```
< body >
```

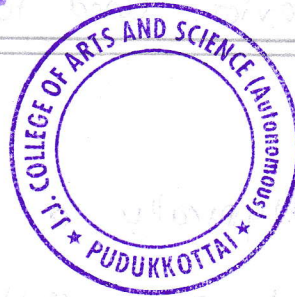
```
< h1 > My First Heading
```

```
< p > My first Paragraph
```

```
< / body >
```

```
< / html >
```

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P. Parasuraman
Dr. J. PARASURAMAN

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S. Venkatesh

Lesson Plan Note

Nov-2021 - May 2022

Even Semester

Unit - I Classes and Objects :

Classes and Objects: Introduction -
Specifying a class - Defining a member function
C++ Program with class - Making an
outside Function Inline - Nesting of member
function - Array within a class - Memory
allocation for objects - static and data
members - Static member function - Array of
objects - Copy Constructor - Dynamic Constructor -
Constructing two dimensional array -
Destructors.

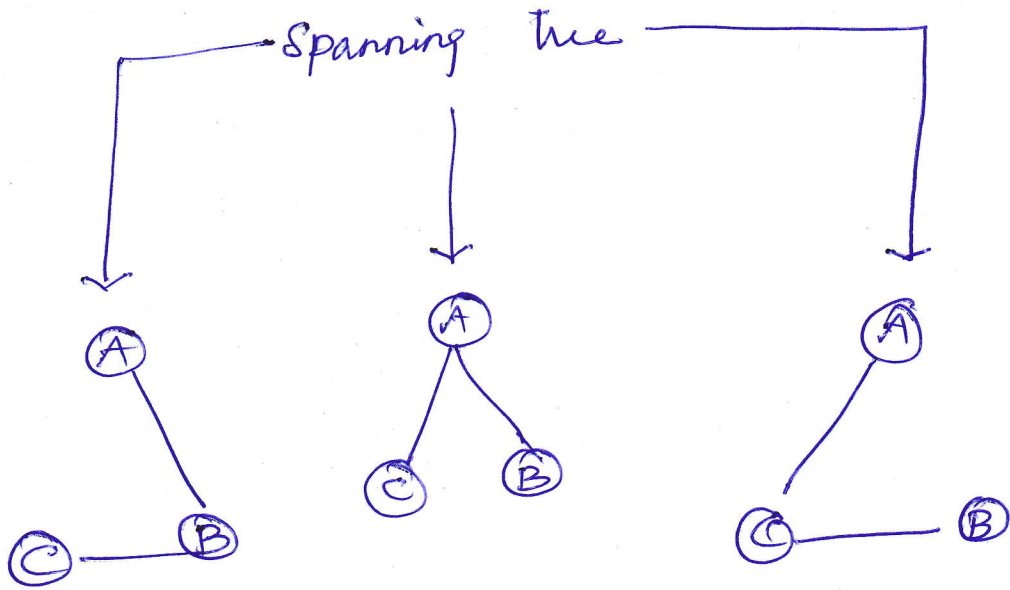
Unit - II Inheritance and Polymorphism

Inheritance: Introduction - Defining Derived
class - Single inheritance - making a Private
member inheritable - Multi level inheritance -
Multiple inheritance - Hierarchical inheritance -
Virtual Base class - Abstract class -
Constructors in derived class - member class:
Nesting of class.

Unit - III Linear Data Structure

Overview - Arrays - Ordered

Stack and Queues: Evaluation of



→ A Complete undirected

Graph can have max n^{n-2} number

of spanning trees, n is the number of

nodes.

G.P.

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SAD
Q. Abinayaki

Assistant Professor.

J. J. C.

(Feb 2022 - May 2022.)

9

B.

PHP SCRIPTING LANGUAGE.

B
C

Unit - I Essentials of PHP

Essentials of PHP - Operators and flow control - strings and Arrays.

Unit - II Functions.

Creating functions. - Reading data in web pages - PHP Browsers - Handling power.

Unit - III Object oriented Programming

Object oriented Programming - Advanced object oriented programming.

Unit - IV Files and Database.

File handling - working with Databases.

Unit - V Sessions and Ajax

Sessions, Cookies and FTP - Ajax - Advanced Ajax.

Text book :-

The PHP complete reference, Steven Holzner, McgrawHill Education, 2007.

Date: 12/5/22

class: 11/12

Day order: D4

hours: 5, 4

⇒ Sessions, Cookies and FTP.

* Setting a cookie.

* Reading a cookie.

* Setting Cookies

Expiration.

* Deleting cookies.

* Working with FTP.

* Downloading files with

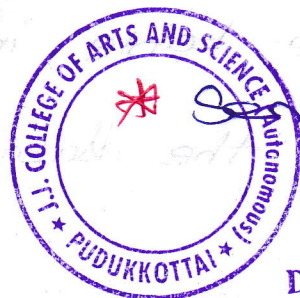
FTP

* Uploading files with

FTP

* Deleting files with

S. Sudha
12/5/22
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* Sending E-mail.

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M.Phil., B.Ed., Ph.D.

Data Base System Concepts

Unit I Introduction

Database System Applications - Purpose of Database System - View of Data - Database Languages - Relational Databases - Database Design - Object-Based and Semi Structured Databases - Data Storage and Querying Transaction Management - Data Mining and Analysis - Database Architecture - Database Users and Administrators - History of Database System.

Unit II Relational Model

Structure of Relational Databases - Fundamental Relational - Algebra Operations Additional Relational - Algebra operations - Extended Relational - Algebra operations - Null Values - Modification of the database

Unit III SQL

Data Definition - Basic structure of SQL Queries - Set operations - Aggregate Functions - Null Values - Nested Sub Queries - Complex Queries - Views - Modification of the database - Joined Relations.

Unit IV Relational Languages

The tuple Relational Calculus - The Domain Relational Calculus - Query-by-Example. Database Design and the ER Model: Overview of the Design Process - The entity - Relationship Model - Constraints - Relationship Diagrams - Weak Entity sets -

Microprocessor and its applications

Objectives :-

Unit - I

Introduction to microprocessor
Evaluation of microprocessors - Single chip Microcomputer - Microprocessor application - programming digital computers - memory - Buses - memory addressing capacity and CPU - micro computers - processor architecture - Intel 8085 - introduction cycle - Timing diagram .

Unit - II

Intel 8085

Introduction set of intel 8085 - Instruction and data formats - Addressing models - Status flags - Intel 8085 Instructions - Programming of microprocessors - Assembly language - Assemblers - Stacks and Subroutines - MACRO - microprogramming .

Unit - III

Examples of Assembly language programs

Assembly language programming - Simple examples - Addition and subtraction of binary and decimal number - complements - Shift - masking - Finding the largest

Software Engineering

unit - i:

Introduction to Software Engineering.

Definitions - size factors - Quality ?

Productivity factors - Managerial issues -

The product - The evolving role of s/w -

s/w characteristics - Appks - The process -

s/w engg: A layered technology - The s/w process - Evolutionary s/w process models - Spiral model.

unit - ii: s/w planning: planning a s/w

project - Defining the pbm - Developing a soln strategy - Planning the devpt process - Planning an organizational structure - other planning activities.

unit - iii: s/w cost estimation & Reqs

Definition

s/w cost estimation: s/w cost factors - s/w cost estimation techniques - staffing level estimation - s/w reqs defn! s/w reqs specification - formal specification Techs.

unit - iv: Design & Implementation tools:

s/w Design: fundamental Design Concepts - Modules & modularization criteria -

Design notation - Design techs - Design Guide lines -

Implementation issues: structured coding techniques - coding style -

Documentation guidelines.

unit - v: testing & validation

main routine & one or two immediately
subroutines in the system structure

- Requires the use of Pgm stubs
to stimulate the effort of lower-
level routines.

Advantages:

1. Top-level interfaces are tested first.
2. Top-level routines provide a natural test harness for lower-level routines.

I.D.
07/12/2024

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SR

Dr. S. RATHNADEN

Assistant Professor.

Dept. of Computer Science.

Digital Image Processing

(Sub. Code: P3RICSC10)

Unit - I Digital Image Fundamentals

Introduction: Fundamental Steps in Digital Image Processing - Components of an Image Processing System - Elements of Visual Perception - Image Sampling and Quantization

Basic concepts in Sampling and Quantization

Representing Digital Images - Spatial and Gray level resolution - Basic relationship between pixels - 1D DFT - 2D DFT.

Unit - II Image Enhancement

Basic Gray Level Transformations: Image negatives - Log transformation - Power Law transformation - Piecewise - Linear transformation

functions - Histogram Processing: Histogram Equalization - Histogram matching - Enhancement using arithmetic/Logic

Operations: Image Subtraction - Image Averaging - Color Image Processing:

Color Fundamentals - Color models

Unit - III Image Restoration.

A model of the image degradation restoration process - Noise Models: Spatial

13/11/21 Revision Learning

1 hr

- Start with a small set of pixels

- then start iteratively merging more pixels.

- Region splitting and merging

16/11/21 Revision (Elements of visual perception)

1 hr

18/11/21 Revision on Types of noises

1 hr

20/11/21 Revision (2019 Question Paper)

1 hr

29/11/21 Revision on Image Restoration

1 hr

1/12/21 Revision on Compression

Ms. M. Jothi

Asst. Prof. Dept of CS

June 2021 to Nov 2021

Unit I - Introduction to Internet

Computers in Business - Networking -
E-mail - Resource sharing - WWW - usenet
- Telnet - Bulletin Board Service - wide
Area Information Service, Internet Technologies:
Modem - Internet Addressing - Physical
Connections - Telephone Lines, Internet
Browsers: Internet Explorer - Netscape
Navigator.

Unit II - Introduction to HTML

Designing a home page - History of HTML
HTML generations - HTML document -
Anchor tag - Hyper Links - Sample HTML
documents. Head and Body Sections: Header
Section - Title - Prologue - Links - Colorful
web page - Comment lines.

Unit III - Designing the Body Section

Header Printing - Aligning the header.
Horizontal rule - Paragraph - Tab settings -
Images and pictures - Embedding PNG format
images. Ordered and Unordered Lists: Lists
- unordered Lists - Heading in a list -
Listed Lists. Table Handling:

Revision - Unit IV

18/12/21

D - V

H - IV

- ordered list

- unordered list

- Table tag

- Designing web page using list and table

28/12/21

D - IV

H - II

Revision - Unit V

- Frames

- frameset Set tag

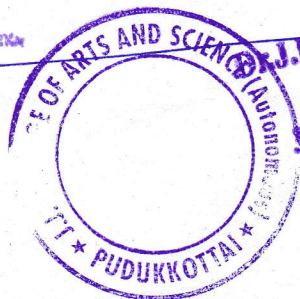
- Frame tag

- Designing web page using frames

S. Sudha 28/12/21

V.D.

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Programming in Java

UNIT - I

OOPS and Java

Introduction to oop - character set - tokens - Constants - Variables - Operators and Expressions - Library Methods - Strings - I/O Statements - Control Statements - If, Switch, while, do, for Statements - Arrays and Methods - one dimensional array - two dimensional array - methods - Method overloading - Recursion.

UNIT - II

Classes, objects and Exception handling

Classes and objects - general form of a class - objects - Constructors - Constructor overloading - This keyword - Finalize Method - Static Methods - Inheritance and Polymorphism - Intensity the variables and methods in class - Exception handling: Default, user defined Exception - try catch, throw statements.

UNIT - III

Interfaces and Packages and Multithreading

Interfaces and Packages: Structure - Implementation

... the classes on

↳ Listeners

↳ Action Listeners

↳ Item Listeners

↳ Mouse Listeners

↳ Methods

↳ Method overloading

↳ Java Database Connectivity

↳ JDBC Basics

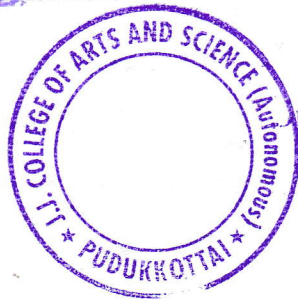
↳ Example Programs

II BSCS
29/12/21
Dayender
IV/8

[Handwritten signature]

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[Handwritten signature]



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2

M. NITHYAKALYANI MEKALA

Notes of Lesson

July 2021 to Nov 2021

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2000

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ing

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ut

as check

king with
ments - mail
merge.

- spread sheets

ms - Building

m water sheets,

ts, creating and

Computer Applications in Business

Unit I

Meaning of computer - characteristics of computer

- Areas of computer applications - I-P-O cycle

Components of computer - Memory and Control

Unit II I/O devices Hardware and SW operating

Systems - Introduction to windows 98 logging

on desktop & taskbar Icons on desktop

Start menu option Creation of files and folders.

windows explorer. Find option short cuts - Briefcase

Running applications and customization

Unit II Word 2000

Starting word 2000 creating Short cut for word 2000

Creating word documents - Creating Business letters

using wizards - Editing word documents - inserting

object formatting documents - spelling and grammar

check. - editing word documents - inserting object

formatting documents - spelling and grammar check

word count thesaurus auto correct working with

tables - saving, opening and closing documents - mail merge.

Unit III

Spread sheets

Introduction to spread sheets - spread sheets

Programmes and applications - features - Building

worksheets - entering data in worksheets, creating and

11/12/21

Inventory

Reports for help

12/12/21

Stock

goto Gateway → Inventory Infn

- Stock groups
- Stock categories
- Stock items
- Unit of Measures
- Godowns
- Voucher types
- Stock
- Single Stock Group
- Multiple Stock Group
- Name
- Under
- Quantities of items to be added



3/12/21

Inr

- Input device
- Output device
- Files, folders

J. Sudha

Director

J. Parasuraman

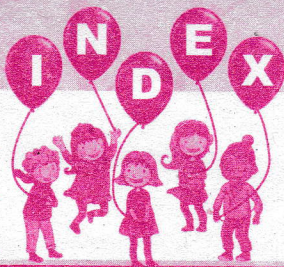
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Windows
Task bar

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 J.J. College of Arts and Science
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 J.J. Nagar, Sivapuram Post,
 PUDUKKOTTAI - 622 422.



V. Nityapriya



 Gold Plus

NAME : _____

STD : _____ SEC : _____

ROLL NO. : _____

S.No.	Date	Title	Page No.	Teacher's Sign / Comment
		July 2021 - Nov 2021		
		Notes of Lesson.		

Java Programming

Unit-I OOPS and Java

Introduction to OOPS:- Character set - tokens - constants - variables - operators and expressions - library methods - strings - IO statement - control statements - if, switch, while, do-while, for statements. - Arrays and methods. - One dimensional array - two dimensional array - methods - method overloading - recursion

Unit-II classes, objects and Exception Handling

Classes and objects:- general format a class - objects - constructors - constructor overloading - this keyword - finalizer method - static methods - inheritance and polymorphism - Inheritance and variables and methods in a class - Exception handling - default user-defined exception - try, catch, throw statements.

Unit - III Interface, Packages & Multithreading

Interface and Packages:- structure, implementation of an interface - Packages - Placing the classes in a package - import statement - the java.lang package - the System Object.

Data Input Stream - Data Output Stream

- ✓ Methods
- ✓ Example

File Input Stream - File Output Stream

- ✓ Methods
- ✓ Example

Reader - Writer

- ✓ Methods
- ✓ Example

Database Connectivity

Example program to connect MS-Access table to Java code

29/11/21
D4/11

Revision of operators in Java
with Example programs.

03/12/21
D5/11/21

Revised Condition statements

- ✓ simple ... if
- ✓ if ... else
- ✓ nested if

Department of Computer Science,

2021 - 2022.

ODD semester

Notes of Lesson.

S.No.	Class	Subject
1.	II. Hsc	Network Security
2.	III. Bsc. "C" & "D"	Data Communication & Networks

Staff Name :- NITHYA.S.

D \ H	I	II	III	IV	V
D ₁	III _C	III _D			II. MSc
D ₂		III _C		II. MSc	III _D
D ₃	III _C		III _D		III _C
D ₄	II. MSc.		III _C	III _D	
D ₅	←	III _C Lab	→		III _D
D ₆	II. MSc			III _C	III _D

Date:- 01/12/2021

Day:- 04

Flour:-

Topic:- Virus structure

- Virus Code implementation
- How it works.
- Prevention Techniques
- Sample Coding for creating Virus.

Date:- 3/12/21

Day:- 05

Flour:-

Topic:- Intruders

- Intruder
 - A threat to the system.
- Types:-
 - Masquerader.
 - Misfeasor.
 - Denial of Service.
- Explained with examples.

J. Deep

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 Department of Computer Science,
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 J. J. College - 622 422.

Date:- 3/12/21

Day

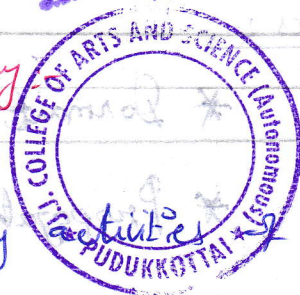
Flour

Topic:- Audit Record

- * Recording day-by-day activities
- * Two Types:-

① Native Audit Record

② Dedicated



Dr. J. PARASURAMAN

PRINCIPAL

Sl No	Date	TITLE	Teacher's Sign/ Remarks
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		Dr. R. BHUVANESWARI	
		Department of Computer Science.	
		JJ College of Arts & Science	

Unit - I .Net Platform

What is ASP.NET? Writing ASP.NET Page - ASP.NET basics: ASP.NET Page structure - view state - working with devices directives - ASP.NET Languages.

Unit - II VB and C# Programming basics.

Programming basics: Control events and Subroutines - Page events variables and variable declaration - Arrays - Functions - operators - Control Logic - Loops - Object oriented Programming Concepts: Object and Classes - Properties and Methods - Classes - Constructors - scope - Events - Inheritance - objects in .NET - Namespace - Using code behind files.

Unit - III Constructing ASP.NET web pages

Web forms - HTML Server Controls - Web Server Controls - web user controls - Master Pages - Cascading Style sheet.

Unit - IV Building web applications and validation Controls.

Visual web developer - Core web Application features - Debugging and Error handling. Using the validation Controls: Require Field Validator - Compare Validator - Range validator - validation Summary - Regular Expression validator -

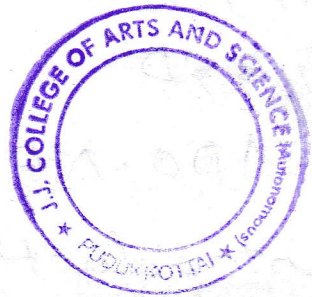
Date: 8/11/21
Class: IV B&D
Topic: ADO.NET and DetailsView

Day order: D1
Hrs: 1st, 3rd

- * Introduction.
- * Example.
- * Row Fields.
- * Binding to Data

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Data operations

V. P. Prasad
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Data Communication and Networks

Syllabus:

Unit I: Introduction

Need to study Data Communications - Data Communications
- Networks - Protocols and Standards - Standards
organizations - Line Configuration Topology - Transmission
Mode - Categories of networks - Internet works.

Unit - II The OSI Model

The Model - Functions of the layers - TCP/IP protocol
suite - Signals: Analog and Digital - Periodic and
Aperiodic signals - Analog signals - Time and Frequency
Domains - Composite Signals - Digital signals.

Unit - III Transmission media

Guided Media - unguided media - Transmission Impairment
- Multiplexing: Many to one/one to many - FDM - TDM
- WDM, Error Detection and Correction - Types of errors
- Detection - Vertical Redundancy check (VRC) -
Longitudinal Redundancy check (LRC) - Cyclic Redundancy
check (CRC) Check sum - Error

Unit - IV Switching

Switching - Circuit Switching
- Time division switches - TDM

- As it encounters each packet, it looks at both the destination and the source addresses. It checks the destination to where to send the packet.

decide

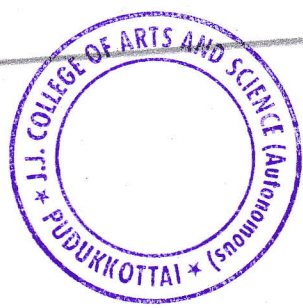
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V.P.

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Name JENIF PREETHI J.

Std _____ Sec _____ Roll.No _____

School Department of Computer

Subject Science

INDEX

No	Date	Title	Marks	signature

June 21 - Nov 21.
Lesson plan.

Course code: U5RICSCC9

UNIT-I: .Net Platform

What is Asp.net? - writing the first

Asp.net Page - Asp.net basics: ASP.net Page

Structure - View State - working with directives -

Asp.net languages.

UNIT-II

VB and C# Programming basics

Programming basics: Control events and

Subroutines - Page events - Variables and variable

declaration - Arrays - functions - Operators -

Conditional logic - Loops - Object oriented

Programming Concepts: objects and classes - Properties

- methods - classes - Constructors - Scope - Events -

Inheritance - Objects in .net - Namespace - Using

Code behind files.

UNIT-III:

Constructing Asp.net web pages

web forms - Html Server controls - Web Server

controls - web user controls - Master pages -

Cascading Style Sheets.

30/11/21.

DA:

Revised GridView control

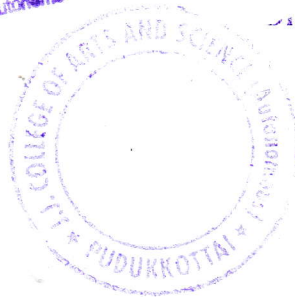
2/12/21: DA.

Revised Asp. Net Page Structure.

4/12/21. DA

S. Sudha Test conducted in Page Structure.
C.D. ✓

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Unit - I

Introduction to operating System

Evaluation of operating System -

Functions - Different views of operating System -

Batch Processing, Multiprocessing, Time Sharing

Operating System - Input/output Programming

Concepts - Interrupt structure & Processing.

Unit - II

Memory Management

Memory Management - Single

Contiguous allocation - Partitioned allocation -

Relocatable Partitions allocations - Page and

Demand Paged memory management -

Segmented memory management - Segmented

and Demand Paged memory management

Overlay techniques - Swapping.

Unit - III

Process Management.

Process Management - Job

Scheduling - Process scheduling - Functions and

Policies - Evaluation of Round Robin Multi -

Programming Performance - Process Synchronization

23/10/21
Unit. IV
III CS D,C
D.O. II
Hr. 1,4

Device Management
* Techniques for device management
* Physical Devices
* Disk devices
* Virtual devices



26/10/21
III CS D,C
Unit. IV
D.O. III
Hr. 1,2

Input Output Traffic Controller.
* Status of I/O Devices
* Control units
* Communication channels.



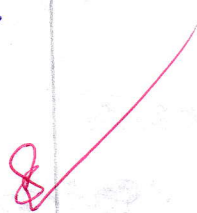
30/11/21
III CS D,C
Unit. IV
D.O. IV
Hr. 4

Control Units
Control Signal
Control bus
Flags
clock.



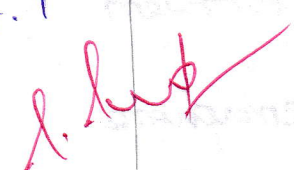
2/12/21
III CS D,C
Unit. IV
D.O. V
Hr. 1,3

Communication Channels:
Request input output status
instruction to IOP
IOP Path.
Memory location



4/12/21
III CS C
Unit. IV
D.O. VI
Hr. 1

Virtual Devices
Network interface Card
Disk Drive
Virtual Box
Virtual Memory techniques.



Unit - I Introduction to operating systems.

Device management - Techniques

Evolution of operating systems - Functions

- Different views of OS - Batch Processing,

multiprocessing, Time sharing OS - I/O

Programming Concepts - Interrupt

Structure & Processing.

Unit - II Memory management

Unit - II Memory management.

Memory management - Single contiguous

Allocation - Partitioned Allocation -

Relocatable Partitions allocations - Paged

and Demand Paged memory management -

Segmented memory management -

Segmented and demand Paged memory

management - Overlay techniques -

Swapping.

Unit - III Processor management.

Processor management - Job scheduling

- Process scheduling - Functions and

Policies - Evolution of Round Robin

multiprogramming Performance -

Process synchronization - wait and

Signal mechanisms - Semaphores P &

V operation - Deadlock - Banker's

Algorithm.

Date: 28/10/21

Class: III D1

Day order: D4

hours: 2

- Important Question revision -

Date: 30/10/21

Class: III B

Day order: D5

hours: 4

- Revision -

Date: 2/11/21

Class: III D1

Day order: D6

hours: 4

Deadly embrace or dead lock:

a) Deadly embrace handling techniques

- * Preallocate all shared resources
- * constrained allocation.

a) controlled allocation

b) standard allocation

I. Luth

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Credit: 03

Network Security

Hours: 60

Objectives :-

- To learn about Cryptographic Techniques.
- To understand the principles of Security
- To incorporate approaches to network Security.

UNIT - 1 Introduction to Classical Encryption Techniques

The OSI - Security Architecture - Security Attacks
- Security Services - Security Mechanisms - A model of Network Security - Classical Encryption Techniques :-
Symmetric Cipher Model - Substitution Techniques -
Transposition - Rotor Machine - Steganography.

UNIT :- 2 Public Key Cryptography.

Principles of public-key Crypto systems -
Applications of public key Cryptosystem - Requirement for public key cryptography - Public key Cryptanalysis
- RSA Algorithm - Description of the Algorithm -
The Security of RSA.

UNIT : 3 Network Security Applications

Electronic Mail Security : Pretty Good Privacy -
PGP Services - Cryptographic Keys & Key Rings -
General format of PGP Message - PGP Msg. Generation -
PGP - Message Reception - RFC-822 - MIME - MIME
Content type - MIME Transfer encoding - S/MIME
functionality - S/MIME Content types - Cryptographic
algorithms - S/MIME Certificate processing.

UNIT - 4 : Web Security.

Web Security Considerations? Web Security Threats
Secure Socket Layer - SSL Architecture -